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Bib Data Sheet

CONFIRMATION NO. 2822

<b>SERIAL NUMBER</b> 09/884,172	<b>FILING DATE</b> 06/19/2001 <b>RULE</b>	<b>CLASS</b> 257	<b>GROUP ART UNIT</b> 2812	<b>ATTORNEY DOCKET NO.</b> ASC-044
<b>APPLICANTS</b> Eugene A. Fitzgerald, Residence Not Provided; Nicole Gerrish, Residence Not Provided;				
<b>** CONTINUING DATA *****</b> THIS APPLN CLAIMS BENEFIT OF 60/250,985 12/04/2000 <i>ka</i>				
<b>** FOREIGN APPLICATIONS *****</b> <i>Wheeler</i>				
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b> <b>** 08/15/2001</b>				
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged Examiner's Signature <i>Chen</i> Initials <i>CS</i>	<b>STATE OR COUNTRY</b>	<b>SHEETS DRAWING</b> 13	<b>TOTAL CLAIMS</b> 27	<b>INDEPENDENT CLAIMS</b> 4
<b>ADDRESS</b> Patent Administrator Testa Hurwitz & Thibault LLP High Street Tower 125 High Street Boston, MA 02110				
<b>TITLE</b> Method of fabricating CMOS inverter and integrated circuits utilizing strained silicon surface channel mosfets				
<b>FILING FEE RECEIVED</b> 1046	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	